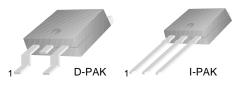


### **KSH45H11**

# General Purpose Power and Switching Such as Output or Driver Stages in Applications D-PAK for Surface Mount Applications

- Lead Formed for Surface Mount Application (No Suffix)
- Straight Lead (I-PAK, "- I" Suffix)
- Electrically Similar to Popular KSE45H
- · Fast Switching Speeds
- Low Collector Emitter Saturation Voltage



1.Base 2.Collector 3.Emitter

### **PNP Epitaxial Silicon Transistor**

### Absolute Maximum Ratings T<sub>C</sub>=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>CEO</sub>	Collector-Emitter Voltage	- 80	V
V <sub>EBO</sub>	Emitter-Base Voltage	- 5	V
I <sub>C</sub>	Collector Current (DC)	- 8	Α
I <sub>CP</sub>	Collector Current (Pulse)	- 16	Α
P <sub>C</sub>	Collector Dissipation (T <sub>C</sub> =25°C)	20	W
	Collector Dissipation (T <sub>a</sub> =25°C)	1.75	W
TJ	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature	- 55 ~ 150	°C

### Electrical Characteristics T<sub>C</sub>=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
V <sub>CEO</sub> (sus)	Collector-Emitter Sustaining Voltage	$I_C = -30 \text{mA}, I_B = 0$	- 80			V
I <sub>CEO</sub>	Collector Cut-off Current	$V_{CE} = -80V, I_{B} = 0$			- 10	μΑ
I <sub>EBO</sub>	Emitter Cut-off Current	$V_{BE} = -5V, I_{C} = 0$			- 50	μΑ
h <sub>FE</sub>	DC Current Gain	$V_{CE} = -1V, I_{C} = -2A$	60			
		$V_{CE} = -1V, I_{C} = -4A$	40			
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	$I_C = -8A, I_B = -0.4A$			- 1	V
V <sub>BE</sub> (on)	Base-Emitter Saturation Voltage	$I_C = -8A, I_B = -0.8A$			- 1.5	V
f <sub>T</sub>	Current Gain Bandwidth Product	$V_{CE} = -10A, I_{C} = -0.5A$		40		MHz
C <sub>ob</sub>	Collector Capacitance	V <sub>CB</sub> = - 10V, f = 1MHz		230		pF
t <sub>ON</sub>	Turn On Time	I <sub>C</sub> = - 5A		135		ns
t <sub>STG</sub>	Storage Time	$I_{B1} = -I_{B2} = -0.5A$		500		ns
t <sub>F</sub>	Fall Time			100		ns

<sup>\*</sup> Pulse Test: PW≤300μs, Duty Cycle≤2%

# **Typical Characteristics**

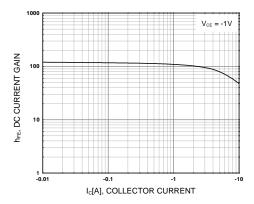


Figure 1. DC current Gain

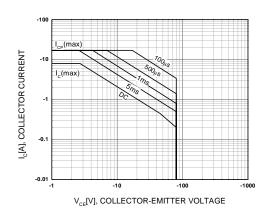


Figure 2. Safe Operating Area

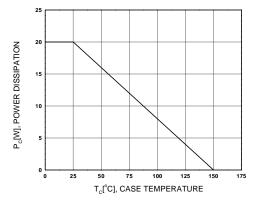
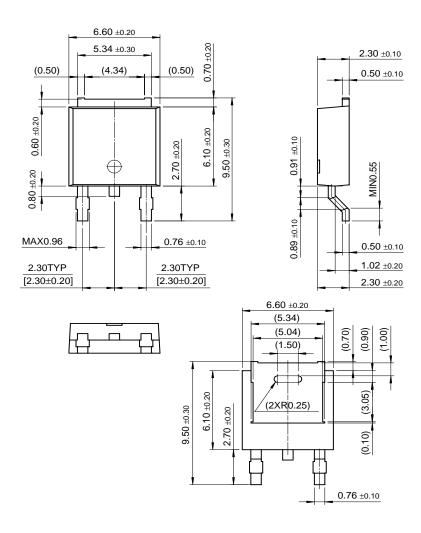


Figure 3. Power Derating

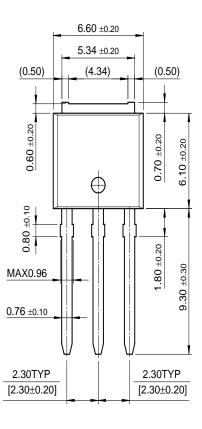
# **Package Dimensions**

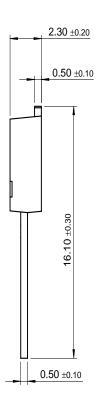
# D-PAK

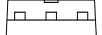


# Package Dimensions (Continued)

# I-PAK







Dimensions in Millimeters

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E <sup>2</sup> CMOS™	HiSeC™	MSXPro™	Quiet Series™	TruTranslation™
EnSigna™	$I^2C^{TM}$	$OCX^{TM}$	RapidConfigure™	UHC™
Across the board.	Around the world.™	OCXPro™	RapidConnect™	UltraFET <sup>®</sup>
The Power Franchise™		OPTOLOGIC <sup>®</sup>	SILENT SWITCHER®	VCX <sup>TM</sup>
Programmable Ad	ctive Droop™	OPTOPLANAR™	SMART START™	

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